

ATTY DOCKET NO.

SERIAL NO.

2380-187

09/829,451

APPLICANT

ANDERSSON et al

(Use several sheets if necessary)

FILING DATE

GROUP

April 10, 2001

2661

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

Call. ~~the~~ 2102105

*Examiner

Date Considered

2/23/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

INFORMATION DISCLOSURE

ATTY. DOCKET NO.

SERIAL NO.

CITATION

2380-187

09/829,451

APPLICANT

ANDERSSON et al

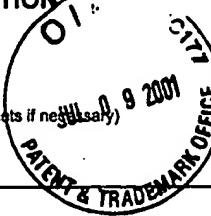
FILING DATE

GROUP

April 10, 2001

2661

(Use several sheets if necessary)



U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CD	5,710,882	1-1998	Svennevik et al.			
CD	5,809,129	9-1998	Andersson et al.			
CD	6,128,295	10-2000	Larsson et al.			
CD	5,790,541	8-1998	Patrick et al.			
CD	5,491,693	2-1996	Britton et al.			
CD	5,987,515	11-1999	Ratcliff et al.			
CD	6,009,103	12-1999	Woundy			
CD	5,963,553	10-1999	Wicklund			
CD	6,088,359	7-2000	Wicklund et al.			
CD	6,154,459	11-2000	Wicklund			
CD	6,034,958	3-2000	Wicklund			
CD	5,963,564	10-1999	Petersen et al.			
CD	5,946,309	8-1999	Westberg et al.			
CD	5,467,347	11-1995	Petersen			
CD	5,361,257	11-1994	Petersen			
CD	5,623,493	4/1997	Kagemoto			
CD	5,570,362	10-1996	Nishimura			
CD	4,692,917	9-1987	Fujioka			
CD	4,973,956	11-1990	Lin et al.			
CD	5,038,343	8-1991	Lebizay et al.			
CD	5,079,762	1-1992	Tanabe			
CD	5,126,999	6-1992	Munter et al.			
CD	5,128,931	7-1992	Yamanaka et al.			
CD	5,140,582	8-1992	Tsuboi et al.			
CD	5,144,293	9-1992	Rouse			
CD	5,680,390	10-1997	Robrock, II			
CD	5,150,358	9-1992	Punj et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
CD	97/41696	11-1997	WO		
CD	0 800 324 A2	10-1997	EP		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

CD	"ATM User-network Interface (UNI) Signaling Specification", Version 4.0, af-sig-0061.00, July 1996, generated by the ATM Forum Technical Committee
CD	ATM Adaptation Layer Switching, Mauger et al, XVI World Telecom Congress Proceedings (XP-000720525) (pp. 207-214).

*Examiner [Signature] Date Considered 2/23/05

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

INFORMATION DISCLOSURE
CITATION

ATTY. DOCKET NO.

SERIAL NO.

2380-187

09/829,451

APPLICANT

ANDERSSON et al

FILING DATE

GROUP

April 10, 2001

2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CLD	5,724,348	3-1998	Basso et al.			
CLD	5,499,239	3-1996	Munter			
CLD	5,740,156	4-1998	Tanabe et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
CLD	0 405 042 A1	1-1991	EP		
CLD	95/30318	11-1995	WO		
CLD	97/33406	12-1997	WO		partial
CLD	0 827 305 A1	3-1998	EP		
CLD	93/19559	9-1993	WO		
CLD	96/34478	10-1996	WO		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

CLD	U.S. Patent Application Serial No. 09/188,101, filed November 9, 1998, "Asynchronous Transfer Mode"
CLD	U.S. Patent Application Serial No. 09/188,102, filed November 9, 1998, Asynchronous Transfer Mode System Handling Differing AAL Protocol"
CLD	U.S. Patent Application Serial No. 09/249,785, filed February 19, 1999, entitled "Establishing Internal Control Paths In ATM Node"
CLD	Mauger et al, "QoS Guarantees for Multimedia Services on a TDMA-Based Satellite Network", IEEE Communications Magazine, July 1997, pgs. 60-65
CLD	DOI et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture with Input and Cross-Point Buffers", ISS '95, World Telecommunications Congress (International Switching Symposium), Advanced Switching Technologies for Universal Telecommunications at the Beginning of the 21 st Century, Berlin, April 23-28, 1995, vol. 1, no. SYMP 15, 23 April 1995, pgs. 384-388
CLD	Weller et al., "Scheduling Nonuniform Traffic in a Packet Switching System with Small Propagation Delay", Proceedings of the Conference on Computer Communications (INFOCOM), Toronto, June 12-16, 1994, vol. 3, 12 June 1994, pgs. 1344-1351
CLD	Eneroth et al, "ATM Transport in Cellular Networks", Iss '97, World Telecommunications Congress (International Switching Symposium), Global Network Evolution: Convergence or Collision? Toronto, Sept. 21-26, 1997, vol. 2, 21 September 1997, pgs. 139-146
CLD	Saito et al, "Layered Cell Structure for ATM Networks", 1996 IEEE International Conference on Communications (ICC), Converging Technologies for Tomorrow's Applications, Dallas, June 23-27, 1996, vol. 3, 23 June 1996, pgs. 1254-1257
CLD	McTiffin et al, "Mobile Access to an ATM Network Using a CDMA Air Interface", IEEE Journal on Selected Areas in Communications, vol. 12, no. 5, 1 June 1994, pgs. 900-908
CLD	Baldwin et al, "AAL-2 - A new ATM Adaptation Layer for Small Packet Encapsulation and Multiplexing", Bell Labs Technical Journal, vol. 2, no. 2, 21 March 1997, pgs. 111-131
*Examiner	Date Considered

Examiner: Initial if reference considered, whether or not citations in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

INFORMATION DISCLOSURE

ATTY. DOCKET NO.

SERIAL NO.

CITATION

2380-187

09/829,451

APPLICANT

ANDERSSON et al

FILING DATE

GROUP

April 10, 2001

2661

U.S. PATENT DOCUMENTS

*EXAMINER
INITIAL

DOCUMENT NUMBER

DATE

NAME

CLASS

SUBCLASS

FILING DATE
IF APPROPRIATE

C/D

5,568,479

10-1996

Watanabe et al.

FOREIGN PATENT DOCUMENTS

TRANSLATION

DOCUMENT

DATE

COUNTRY

CLASS

SUBCLASS

YES

NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

C/D Stephens et al, "Large-Scale ATM Switching Systems for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1157-1160

C/D Chao, "A Recursive Modular Terabit/Second ATM Switch", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1161-1172

C/D Tobagi et al, "Architecture, Performance, and Implementation of the Tandem Banyan Fast Packet Switch" IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1173-1193

C/D Urushidani, "Rerouting Network: A High-Performance Self-Routing Switch for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1194-1204

C/D Yang et al., "A Reconfigurable ATM Switch Fabric for Fault Tolerance and Traffic Balancing", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1205-1217

C/D Itoh, "A Fault-Tolerant Switching Network for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1218-1226

C/D Banwell et al., "Physical Design Issues for Very Large ATM Switching Systems", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1227-1238

C/D Kozaki et al, "32X32 Shared Buffer Type ATM Switch VLSI's for B-ISDN's", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1239-1247

C/D Shobatake et al, "A One-Chip Scalable 8 * 8 ATM Switch LSI Employing Shared Buffer Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1248-1254

C/D Banniza et al, "Design and Technology Aspects of VLSI's for ATM Switches", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1255-1264

C/D Katevenis et al, "Weighted Round-Robin Cell Multiplexing in a General-Purpose ATM Switch Chip", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1265-1279

C/D Giacopelli et al, "Sunshine: A High-Performance Self-Routing Broadband Packet Switch Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1289-1298

C/D Fischer et al, "A Scalable ATM Switching System Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1299-1307

C/D Matsunaga et al, "A 1.5 Gb/s 8X8 Cross-Connect Switch Using a Time Reservation Algorithm" IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1308-1317

C/D Schroeder et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1318-1335

C/D Stavrakakis, "Efficient Modeling of Merging and Splitting Processes in Large Networking Structures", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1336-1347

C/D Cisneros et al, "A Large ATM Switch Based on Memory Switches and Optical Star Couplers", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1348-1360

C/D Itoh et al, "Practical Implementation and Packaging Technologies for a Large-Scale ATM Switching Systems", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1280-1288

*Examiner

Date Considered

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)